

Attachment E – Resolved Discrepancy Reports

The following SIMSS Discrepancy Reports have been fixed in SIMSS Release 4.1. Note that there are 20 DRs (in bold phase) that have been fixed but are still pending Independent Test. The table includes the DR Number, Description, and Severity. The detailed DR information is available in SIMSS CM tool StarTeam.

Summary of Fixed Discrepancy Reports

High	Medium	Low	Total
3	17	14	34

DR	Description	Severity
265	(Client Core) DR12-1: When the Client software is initialized a date and time appears in the SIMSS Client Window Header. What date and time does this relate? It is not the release date or the current date. (R4IT-DR1)	Low
266	(OutputIP) DR12-2: The system is not able to connect via the second Ethernet card. (DT9) (R4IT-DR2).	Medium
267	(AvtecOutput) DR13-1: System will not switch to external clock mode (DT-3.7 failed) - (R4IT-DR3).	Medium
268	(AvtecInput) DR13-2: Avtec input status display does not perform the following. DT4.4a : No Enable/Disable Status. DT4.4b: No indication of Data Frequency. DT4.4c: Frame Length is not displayed. There is a spot for it, but no value is displayed. DT4.4I: We have no idea where this is displayed or why it is a requirement here. (R4IT-DR4)	Low
269	(AvtecOutput) DR13-3: DT4.5 Fails. The boards will not accept NRZ-M or NRZ-S data. The board always appears to be in APC Mode. Changing the mode in the configuration menu for APC has no effect. Also inverting the data has no effect. D through E should be deleted. (R4IT-DR5)	Medium
271	(AvtecInput) DR13-5: There are some problems with the Sync Detect logic. The system needs 32 bits of both sync and mask to properly lock on the data. If you enter the following: 24 bit Size, the system will put the last byte of the previous frame in the first byte of the buffer. Frame Sync will be put in the next three bytes of the buffer. If you configure for 32 bits of sync, the data will be properly aligned in the buffer. The Size field for the sync length does not seem to play a role in the functioning of this module. (R4IT-DR7)	Medium
272	(TDMGen) DR14-1: In testing we encountered an old problem. The displays do not accurately reflect the system status. Data was started in TDMGen and the border of Channel 1 went green. The status indicator shows the Status as Disabled. This has happened since Release 1.0 and has not yet been fixed. (R4IT-DR8).	High
273	(SerialOutput) DR14-2: When Convolution Encoding mode is selected, no data is transmitted. This turned out to be a limitation of the module. The module will not send out any data if the frame length is greater than 2048 bytes. (R4IT-DR9).	Medium
274	(SerialInput) DR14-3: The User's Guide does not reflect the Release 4.0. It does not accurately give the user the information needed to run this release. For example on the Serial Input Module. Which board does this relate to? It would help the general user if the module name changed to ICS Serial Input Module from Serial Input Module. This would be consistent with the AVTEC Serial Input Module. The Parameter for Frame Size does not exist on the Serial Input	Medium

	Configuration Window. (R4IT-DR10). (Note: The SUG has been updated to reflect the problem mentioned)	
275	(SerialInput) DR14-4: DT 6 The ISC Serial Input board does not support TTL. This requirement needs to be corrected. (R4IT-DR11). (Note: No software changes).	Low
277	(AvtecInput) DR14-6: System crashes when you attempt to reconfigure the AVTEC Serial Input Module. This did not occur during individual testing of the module. (R4IT-DR13).	Medium
278	(AvtecInput) DR14-7: The Data Dump on the AVTEC Serial Input module only displays 256 bytes of data. (R4IT-DR14).	Medium
279	(SerialOutput) DR15-1: The ICS card will not properly transmit buffers containing odd number of bytes. (R4IT-DR15).	Medium
281	(GenTlm) DR15-3: Because of the varying lengths of the VC's, it will not work properly with the Serial Output Module. (R4IT-DR17). (Will fix VC's length)	Medium
283	(VC Processor) DR15-6: The Virtual Channel Processor did not properly display the number of frames received. (R4IT-DR19). (Can't reproduced)	Medium
284	(Packet Processor) DR15-7: The Packet Processor does not appear to process Packets in the 1100 series. (R4IT-DR20).	Medium
285	(DTMGen) DR16-1: TDMGEN The reset counters switch does not reset the counters. (R4IT-DR21).	Low
287	(TDMCmdIngest) DR16-3: Configured Command Ingest for no postamble (0 length). Received the following message. 047:14:21:07 Proj 0 TDMCmdIngest DLL: Warning: Could not find the Postamble Sequence, should be: (hex) Since there was no postamble, this message should not have occurred. (R4IT-DR23).	Low
288	(TDMCmdIngest) DR16-4: The only way to reconfigure TDM Command Ingest module is to stop the entire system. This should be changed. (R4IT-DR24).	Medium
289	(TDM DQM) DR16-5: The Output displays need to have the ability to be updated while the system is running. (Not really in the requirements, but for the module to be useful, this needs to be done.) (R4IT-DR25).	Low
290	(TDM DQM) DR16-6: The list button in the output configuration display appears to do nothing. (R4IT-DR26).	Low
291	(CmdXmit) When using two CmdXmit modules, get error messages concerning packet container items. (R4ST-DR1)	Low
295	(AvtectInput) the encoding on Avtec Serial Input module did not work, i.e. NRZM, NRZS. (R4ST-DR5).	Low
296	(Encoding) CRC-16 encoding not working on the encoding module. (R4ST-DR6).	Low
297	(SerialOutput) CRC-16 encoding not working on the serial output module. (R4ST-DR7).	Low
298	(SerialOutput) ICS Serial Output module cannot output frames with odd number of bytes. (R4ST-DR8).	Low
299	(Generic CmdIngest) DR 19-1: Command Status always indicates "disabled". (R4IT-DR27).	Medium
300	(Generic CmdIngest) DR 19-2: We need positive event messages when commands are being processed without any errors. At a minimum, BD type commands should be displayed. Once this module can be database driven the other types of commands will be taken care of. The only way to tell if commands are being processed is by checking the REPVAL in the CLCW. (R4IT-DR28).	High
301	(Generic CmdIngest) DR 19-3: Container buffers are not displayed when using the getbuffer directive. (R4IT-DR29).	Medium
302	(Generic CmdIngest) DR 19-4: The 32 bits of CLCW that this module generates does not report the REPVAL at all. None of the flag bits are reported. No matter	High

	what flag bit is set, only the "bit lock" bit is reflected in the telemetry. (R4IT-DR30).	
303	(Generic CmdIngest) DR 19-5 There seems to be no way to determine the rate of CLCW output let alone suspend/resume the update. The CLCW should be constantly updated. (R4IT-DR31).	Medium
305	(Log) DR20-2: This module does not have a buffer dump in the Run Time pop up window. It appears the only way to meet this requirement is to add a monitor module. (R4IT-DR33).	Low
307	(TxFile) DR20-4: Manual mode is not working properly. (R4IT-DR35).	Medium
311	(Server Core) When SIMSS is shut down, the last 20 or so event msgs to not get written to the log file. This is true even if shutdown is proper (by the numbers).	Low