

Attachment F – Unresolved Discrepancy Reports

All open SIMSS Discrepancy Reports (DRs) are listed in the following table. These consist of DRs that remain open from Release 1.0 through Release 6.0 (not including DRs found during MOSA testing). The table includes the DR Number, Description, Category, and Severity. The Category describes where the DR was first reported, the SIMSS release it affected, and the test team that reported it (ST = system test; IT = independent test). The lists are divided into Open, Fixed, and Verified. The Open DRs have not been addressed yet, the Fixed and Verified have been addressed but not tested by the system and independent test teams due to time constraints.

Summary of Open Discrepancy Reports

High	Medium	Low	Total
3	27	13	43

OPEN DR LIST: Release 6.0

DR	Description	Category	Severity
#67	Add version control to server modules.	SIMSS R4.0 STDR	Medium
#150	We are losing HANDLES and THREADS after we remove modules from the Project. Two Threads are typical. GenericTLM and SCPM1 are losing thousands of HANDLES.	SIMSS R2.0 STDR	Medium
#159	Design and develop new Java Host Server and thin Java Client	SIMSS R2.0 STDR	Medium
#163	Provide base for numbers in event log or standardize on one base.	SIMSS R2.0 STDR	Medium
#206	Add a graphical (vs. text-based) method to configure the wrapper and stripper modules.	SIMSS R2.0 STDR	Medium
#207	Add capability to make incidental configuration changes (such as adding a Log or Monitor module) without stopping.	SIMSS R2.0 ITDR	Medium
#211	A text floating point number prefixed by a 0 (e.g., 0.125) is not correctly converted into an RmmContainerNumeric<float>, though without the prefix (".125").	SIMSS R2.0 ITDR	Medium
#219	Unable to lock on data when the Interval in the TXFile module is less than 10 msec. This problem occurs with data either from CD or hard drive - (IT-DR1)	SIMSS R3.0 ITDR	Medium
#232	Reverse Order did not work for a large size file. This needs to be redone,i.e. file reversal should be done prior to transmission - Regression Test R15.5: TXFile Module: Reverse Order.	SIMSS R3.0 STDR	Medium
#235	Pause/resume not applicable to every SIMSS modules.	SIMSS R3.0 STDR	Low
#264	The Auto-Polarity Correction (APC) capability was found to be non-functional during MPS/Aqua Release 6.2 independent test. The module would default to normal polarity.	AQUA R3.0 ITDR	Medium
#270	DR13-4: The Avtec input status display is lacking displays for items DT4.6 a, b, c, and v. Why DT4.6v is here does not make any sense. (R4IT-DR6)	SIMSS R4.0 ITDR	Low
#280	DR15-2: Until the fill VC's are implemented this module cannot be realistically be connected to a serial output module. The Fill VC's are required to maintain the contiguous data stream. (R4IT-DR16).	SIMSS R4.0 ITDR	Medium
#281	DR15-3: Because of the varying lengths of the VC's, it will not work properly with the Serial Output Module. (R4IT-DR17).	SIMSS R4.0 ITDR	Medium
#282	DR15-4: Packet Sequence errors. We are seeing packet sequence errors.	SIMSS R4.0 ITDR	Medium

	These seem to occur more when the packet size is larger than the virtual channel size. (R4IT-DR18).		
#292	DR: using same ICS card for serial output and serial input caused crash at high rates. (R4ST-DR2).	SIMSS R4.0 STDR	Low
#306	DR20-3: At end of playback the Transmit screen does not reflect that it has completed by changing the send button back to a mode that will accept input. (R4IT-DR34).	SIMSS R4.0 ITDR	Low
#308	DR20-5: Auto-Blocks Mode is not working in serial transmission mode. (R4IT-DR36). Note: Doesn't work for log files either.	SIMSS R4.0 ITDR	Medium
#309	NOTE 20-1: It would be helpful to the user if the buffer display addresses would default to decimal. The data should default to hex, but the address should be displayed in decimal.(R4IT-EH1).	SIMSS R4.0 ITDR	Low
#325	The ! (negate) operator does not always work. By this I mean if (!(a==0)) is true, then (!(!(a==0))) should be false and so on.	SIMSS R5.0 STDR	Medium
#349	The problems are, on the Primary Header and Secondary Header read screens. When you do expand the window so you can select the file (where you see the file system), the screen refreshes once a second (or less) and it removes your selection and/or scrolls the window back to the top. This makes it so you have to select and apply your selection in less than a second.	SIMSS R5.0 STDR	Low
#350	Also, the Secondary header does not restore the data correctly), when you do the save/read from the Build CLTU window. When you do the Save/Read on the Modify Cmd Secondary window, the data will not restore, saying that "Can not load. Must enter length to read." (it also doesn't restore the correct data if you enter the length)	SIMSS R5.0 STDR	Medium
#351	When inducing errors, the boxes did not turn red (they are supposed to in order to inform the user that there wre errors).	SIMSS R5.0 STDR	Low
#352	When opening the the status window, there was an invalid mnemonic.	SIMSS R5.0 STDR	Low
#354	pktProcessor won't forward packets	SIMSS R5.0 STDR	Medium
#359	SerialInput module failed to receive data with 192 kbps - 240 bytes in frame length, having 100 minor frames per major frame. The overnite run shows frame drops as well as frame errors.	MOSA R6.0 ITDR	Medium
#364	The maximum number of modules in a project needs to be greater than 21.	SIMSS R6.0 STDR	Low

Total: 27

FIXED DR LIST: Release 6.0

DR	Description	Category	Severity
#313	If GenTlm is not configured correctly to the ICS serialoutput module, the server will crash when stopping the project. (R4.1-ST1).	SIMSS R4.0 ITDR	Medium
#321	The TlmMod module needs the capability to calculate CRC for use with TDM projects such as HOST, EUVE, UARS. A temporary work-around for HOST is in place and has been tested. A permanent solution would be having a generic CRC that TDMGen has implemented.	SIMSS R4.2 STDR	Medium
#338	Prior to R5, a mechanism was in place that provided a real CLCW to each Virtual Channel in GenTlm that reported the current status of the corresponding command Virtual Channel. This CLCW was generated and transmitted by GenCmdIngest module to report ongoing CLTU processing. This capabilty was "unplugged" by R5 changes and needs to reattached. Until it is, there is no way for GenTlm to report any CmdIngest activities.	SIMSS R5.0 STDR	Medium
#339	The GenTlm module does not provide a save and restore capability.	SIMSS R5.0 STDR	Low
#341	The Error Control field (CRC) cannot be turned off unless R/S is turned on. Several S/C do not use either of these fields. We need to be able to select the Error Control field on or off.	SIMSS R5.0 OTDR	Low

Total: 5

VERIFIED DR LIST: Release 6.0

DR	Description	Category	Severity
#267	DR13-1: System will not switch to external clock mode (DT-3.7 failed) - (R4IT-DR3).	SIMSS R4.0 ITDR	Medium
#271	DR13-5: There are some problems with the Sync Detect logic. The system needs 32 bits of both sync and mask to properly lock on the data. If you enter the following: 24 bit Size, the system will put the last byte of the previous frame in the first byte of the buffer. Frame Sync will be put in the next three bytes of the buffer. If you configure for 32 bits of sync, the data will be properly aligned in the buffer. The Size field for the sync length does not seem to play a role in the functioning of this module. (R4IT-DR7)	SIMSS R4.0 ITDR	Medium
#273	DR14-2: When Convolution Encoding mode is selected, no data is transmitted. This turned out to be a limitation of the module. The module will not send out any data if the frame length is greater than 2048 bytes. (R4IT-DR9).	SIMSS R4.0 ITDR	Medium
#275	DR14-4: DT 6 The ISC Serial Input board does not support TTL. This requirement needs to be corrected. (R4IT-DR11).	SIMSS R4.0 ITDR	Low
#284	DR15-7: The Packet Processor does not appear to process Packets in the 1100 series. (R4IT-DR20).	SIMSS R4.0 ITDR	Medium
#300	DR 19-2: We need positive event messages when commands are being processed without any errors. At a minimum, BD type commands should be displayed. Once this module can be data base driven the other types of commands will be taken care of. The only way to tell if commands are being processed is by checking the REPVAL in the CLCW. (R4IT-DR28).	SIMSS R4.0 ITDR	High
#302	DR 19-4: The 32 bits of CLCW that this module generates does not report the REPVAL at all. None of the flag bits are reported. No matter what flag bit is set, only the "bit lock" bit is reflected in the telemetry. (R4IT-DR30).	SIMSS R4.0 ITDR	High
#303	DR 19-5 There seems to be no way to determine the rate of CLCW output let alone suspend/resume the update. The CLCW should be constantly updated. (R4IT-DR31).	SIMSS R4.0 ITDR	Medium
#307	DR20-4: Manual mode is not working properly in serial transmission mode. (R4IT-DR35).	SIMSS R4.0 ITDR	Medium
#312	The Nand and Nor functions do not work	SIMSS R4.0 STDR	Low
#317	Entering a set directive using one of the arithmetic functions causes a server crash if the telemetry mnemonic is mis-spelled.	SIMSS R4.0 STDR	High

Total: 11