Attachment F – Unresolved Discrepancy Reports

All open SIMSS Discrepancy Reports (DRs) are listed in the following table. These consist of DRs that remain open from Release 1.0 through Release 7.0 (not including DRs found during MOSA testing). The table includes the DR Number, Description, Category, and Severity. The Category describes where the DR was first reported, the SIMSS release it affected, and the test team that reported it (ST = system test; IT = independent test). The lists are divided into Open, and Fixed. The Open DRs have not been addressed yet, the Fixed have been addressed but not tested by the system and independent test teams due to time constraints.

Summary of Open Discrepancy Reports

High	Medium	Low	Total
2	20	16	38

OPEN DR LIST: Release 7.0

DR	Description	Category	Severity
#349	The problems are, on the Primary Header and Secondary Header read screens. When you do expand the window so you can select the file (where you see the file system), the screen refreshes once a second (or less) and it removes your selection and/or scrolls the window back to the top. This makes it so you have to select and apply your selection in less than a second	SIMSS R5.0 STDR	Low
#351	When inducing errors, the boxes did not turn red (they are supposed to in order to inform the user that there wre errors).	SIMSS R5.0 STDR	Low
#352	When opening the the status window, there was an invalid mnemonic.	SIMSS R5.0 STDR	Low
#384	The defaults for the HSTCmdIngest validation criteria need to be changed.	SIMSS R7.0 STDR	Low
#410	The Reset All button only clears the parameters of the last VC on the status display.	MOSA 1.0 ITDR	Low
#411	The module defaults to CRC validation. No error code validation should take place without user intervention	MOSA 1.0 ITDR	Low
#412	When the module is restored, the configuration display does not reflect the stored values. The module does restore correctly.	MOSA 1.0 ITDR	Low
#414	The VC configuration does not restore correctly. Looks like it restores to the original default values.	MOSA 1.0 ITDR	Low
#415	All packets are enabled at initialization even though packet configuration indicates they are inactive. Packets should only be active through user intervention.	MOSA 1.0 ITDR	Low
#416	I could not disable packets using the button in the packet configuration window.	MOSA 1.0 ITDR	Low
#417	When I enabled packets using the buttons in the packet configuration window, I started receiving debug messages on the server display each time one of the selected packets was received. With a lot of packets active, the system became unstable and eventually crashed. This debug statement needs to be removed.	MOSA 1.0 ITDR	Low
#418	The Packet Processor indicated it received packets that were not turned on in the test data stream. Verified with another packet processor.	MOSA 1.0 ITDR	Low
#419	The displayed interval time between the receipt of the same packet was incorrect.	MOSA 1.0 ITDR	Low
#420	The Packet Processor indicated sequence errors between packets of the same type. Verified these sequence errors did not exist using another	MOSA 1.0 ITDR	Low

	packet processor.		
#421	The Packet Processor configuration display gives erroneous indications for the displayed packet ranges. Sometimes the indicators stayed green even though there were errors. Some stayed red even though there were no errors. Some changed colors even though there was no activity for a particular packet range.		Low
#211	A text floating point number prefixed by a 0 (e.g., 0.125) is not correctly converted into an RmmContainerNumeric<float>, though without the prefix (".125").	SIMSS R3.0 ITDR	Medium
#215	SerialInput status window should not show "Lock" or "Search" when the project is stopped.	SIMSS R3.0 STDR	Medium
#219	Unable to lock on data when the Interval in the TXFile module is less than 10 msec. This problem occurs with data either from CD or hard drive - (IT-DR1)	SIMSS R3.0 ITDR	Medium
#232	Reverse Order did not work for a large size file. This needs to be redone,i.e. file reversal should be done prior to transmission - Regression Test R15.5: TXFile Module: Reverse Order.	SIMSS R3.0 STDR	Medium
#271	DR13-5: There are some problems with the Sync Detect logic. The system needs 32 bits of both sync and mask to properly lock on the data. If you enter the following: 24 bit Size, the system will put the last byte of the previous frame in the first byte of the buffer. Frame Sync will be put in the next three bytes of the buffer. If you configure for 32 bits of sync, the data will be properly aligned in the buffer. The Size field for the sync length does not seem to play a role in the functioning of this module. (R4IT-DR7)	SIMSS R4.0 ITDR	Medium
#280	DR15-2: Until the fill VC's are implemented this module cannot be realistically be connected to a serial output module. The Fill VC's are required to maintain the contiguous data stream. (R4IT-DR16).	SIMSS R4.0 ITDR	Medium
#281	DR15-3: Because of the varying lengths of the VC's, it will not work properly with the Serial Output Module. (R4IT-DR17).	SIMSS R4.0 ITDR	Medium
#282	DR15-4: Packet Sequence errors. We are seeing packet sequence errors. These seem to occur more when the packet size is larger than the virtual channel size. (R4IT-DR18).	SIMSS R4.0 ITDR	Medium
#325	The ! (negate) operator does not always work. By this I mean if (!(a==0)) is true, then (!(!(a==0))) should be false and so on.	SIMSS R5.0 STDR	Medium
#350	Also, the Secondary header does not restore the data correctly), when you do the save/read from the Build CLTU window. When you do the Save/Read on the Modify Cmd Secondary window, the data will not restore, saying that "Can not load. Must enter length to read." (it also doesn't restore the correct data if you enter the length)	SIMSS R5.0 STDR	Medium
#354	pktProcessor won't forward packets	SIMSS R5.0 STDR	Medium
#359	SerialInput module failed to receive data with 192 kbps - 240 bytes in frame length, having 100 minor frames per major frame. The overnite run shows frame drops as well as frame errors.	MOSA R6.0 ITDR	Medium
#369	If we receive continuous error/event messages, the event display and event logging processor become overloaded and will cause the system to crash.	MOSA R6.0 ITDR	Medium
#374	The Delta Time display is not working on the Block Monitor module.	MOSA R6.0 ITDR	Medium
#378	Any error entered in the fields should be determined immediately instead of waiting for hitting "Apply"	MOSA R6.0 ITDR	Medium
#379	The Wrapper module should use the db_location.txt file to point to the Wrapper configuration modules.	MOSA R6.0 ITDR	Medium
#385	In using the getbuffer directive while testing, leaving off the 'count' paramete locks up PC.:	SIMSS R7.0 DTDR	Medium
#370	The Avtec Serial Modules are corrupting the data being transmitted when	MOSA R6.0 ITDR	High

	data is being received. This is occurring at higher rates but well within the requirements of MOSA.		
#371	The ticks between TDMGEN and the Serial Output modules appear to get lost. This causes the data to stop and the system needs to be restarted to correct this problem.	MOSA R6.0 ITDR	High

FIXED DR LIST: Release 7.0

DR	Description	Category	Severity
#341	The Error Control field (CRC) cannot be turned off unless R/S is turned on. Several S/C do not use either of these fields. We need to be able to select the Error Control field on or off.	SIMSS R5.0 OTDR	Low
#338	Prior to R5, a mechanism was in place that provided a real CLCW to each Virtual Channel in GenTlm that reported the current status of the corresponding command Virtual Channel. This CLCW was generated and transmitted by GenCmdIngest module to report ongoing CLTU processing. This capabilty was "unplugged" by R5 changes and needs to reattached. Until it is, there is no way for GenTlm to report any CmdIngest activities.	SIMSS R5.0 STDR	Medium
#313	If GenTlm is not configured correctly to the ICS serialoutput module, the server will crash when stopping the project. (R4.1-ST1).	SIMSS R4.0 ITDR	Medium
#321	The TlmMod module needs the capability to calculate CRC for use with TDM projects such as HOST, EUVE, UARS. A temporary work-around for HOST is in place and has been tested. A permanent solution would be having a generic CRC that TDMGen has implemented.	SIMSS R4.2 STDR	Medium